

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

ATTORNEY DOCKET NUMBER: 200304186-2

IN RE APPLICATION OF: U.S. Patent No. 7,100,210

USPTO CONFIRMATION CODE: 9108

APPLICATION NO.: 09/923,192

FILED: 08-06-2001

EXAMINER: Kristin D. Sandoval

GROUP ART UNIT: 2132

FOR: HOOD INTRUSION AND LOSS OF AC POWER DETECTION WITH
AUTOMATIC TIME STAMP

37 CFR 1.322 & 37 CFR 1.323 REQUEST FOR CERTIFICATE OF CORRECTION

HONORABLE COMMISSIONER OF PATENTS & TRADEMARKS

Sir:

The following is a request for a certificate of correction in Serial Number
09/923,192, now Patent Number 7,100,210.

A certificate of correction under 35 USC 254 is respectfully requested in the
above-identified patent.

☒ All errors were the fault of the USPTO, no fee required. In the event that a further
fee is required, please charge the amount to Deposit Account No. 082025.

☐ All errors were the fault of the applicant and, accordingly, please charge **\$100.00**
to our Deposit Account No. 082025. In the event that a further fee is required, please
charge the amount to the same Deposit Account.

☐ The errors were the fault of both the applicant and USPTO and, accordingly,
please charge **\$100.00** to our Deposit Account No. 082025. In the event that a further fee
is required, please charge the amount to the same Deposit Account.

The exact locations where the errors appear in the patent and patent application
are as follows:

On the face page, in field (56), below "U.S. Patent Documents", in column 2, line 11, below "6,289,456 B1 * 9/2001 Kuo et al. 726/34"
insert - -

OTHER PUBLICATIONS

"Nonvolatile Timekeeping RAM", Dallas Semiconductor Data Sheet DS1642,
3/16/98. Page10. - -.

(List of references cited by applicant and considered by examiner dated
Jan. 11, 2006, page 1, under "Other Art", entry 1)

The requested corrections are attached on Form PTO 1050.

August 10, 2010

DATE

Respectfully Submitted



Name: Reed J. Hablinski
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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 7,100,210

APPLICATION NO. : 09/923,192

ISSUE DATE : Aug. 29, 2006

INVENTOR(S) : Sung Hsia Kuo, et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), below "U.S. Patent Documents", in column 2, line 11, below "6,289,456 B1 * 9/2001 Kuo et al. 726/34"
insert - -

OTHER PUBLICATIONS

"Nonvolatile Timekeeping RAM", Dallas Semiconductor Data Sheet DS1642, 3/16/98.
Page10. - -.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P. O. Box 272400
Fort Collins, Colorado 80527-2400

This collection of information is required by 37 CFR 1.322, and 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patent, P.O. Box 1450, Alexandria, VA 22313-1450.**

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.